

to function - it contains a complex arrangement of resistors, transistors, capacitors, and diodes, but we will only study the behavior of ideal op-amps as one device and what occurs at its terminals. There are five terminals found on all op-amps: - the inverting input - the non-inverting input - the output No connection Balance Inverting input 2 0 - the (+) and (-) power supplies 7 🗆 V+ Noninverting input 3 Inverting input Noninverting input 6 Output Balance V^{\cdot}

Offset Null

Usually on circuit diagrams, the power supply terminals are obscured and it is taken for granted that they are there. In addition, they usually use two voltage sources with a ground reference between them - this gives a positive and negative supply voltage.



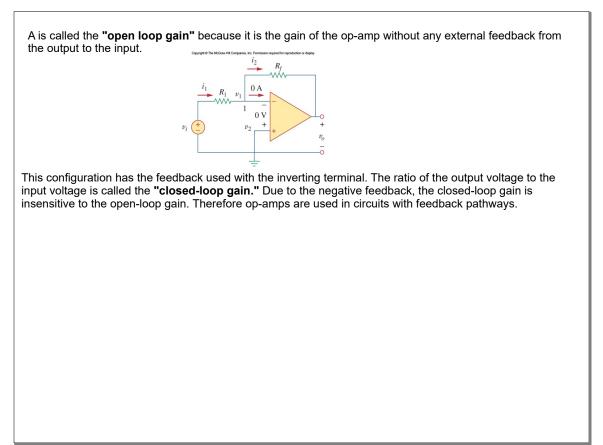
The voltage output of an op-amp is proportional to the difference between the non-inverting and inverting inputs.

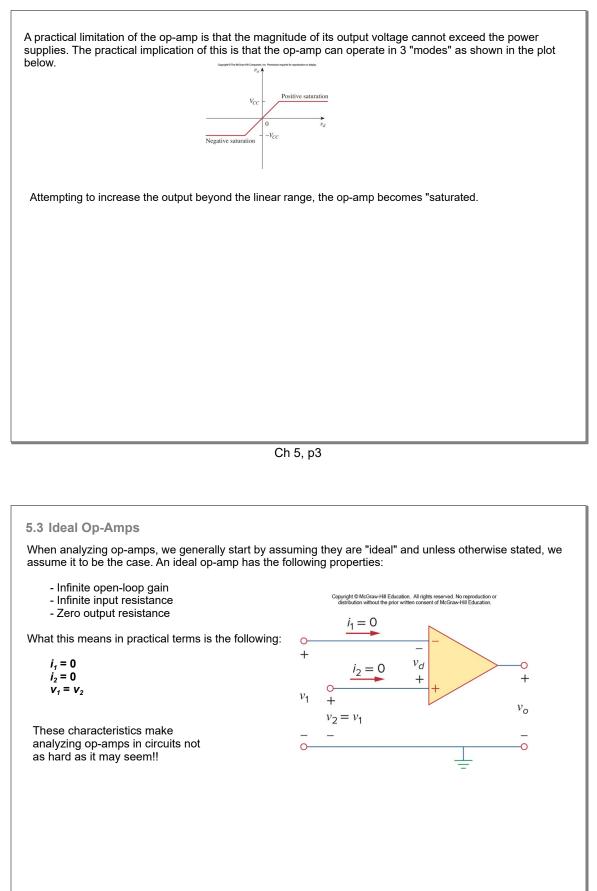
$$v_0 = Av_d = A(v_2 - v_1)$$

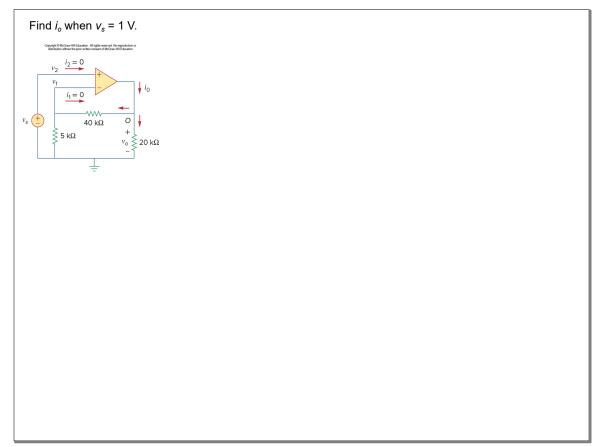
Parameter	Typical range	Ideal values
Open-loop gain, A	10^5 to 10^8	∞
Input resistance,R_i	10^5 to 10^13 ohms	∞ ohms
Output resistance, R_0	10 to 100 ohms	0 ohms
Supply voltage,V_cc	5 to 24 V	

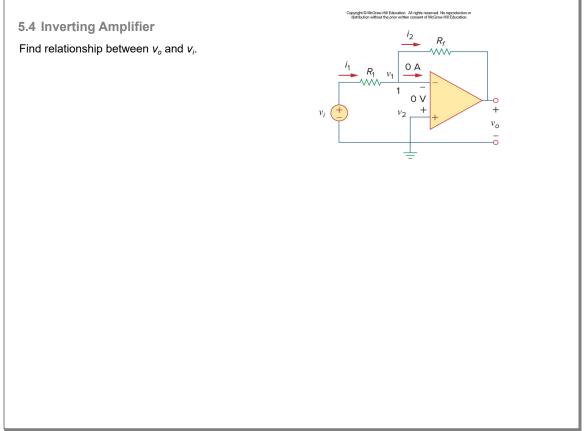
A is called the "open loop gain."

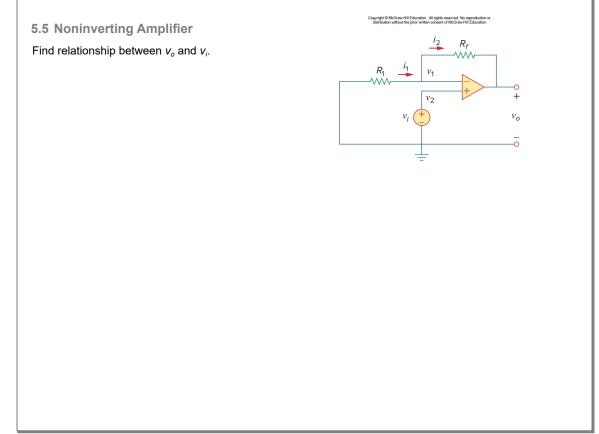




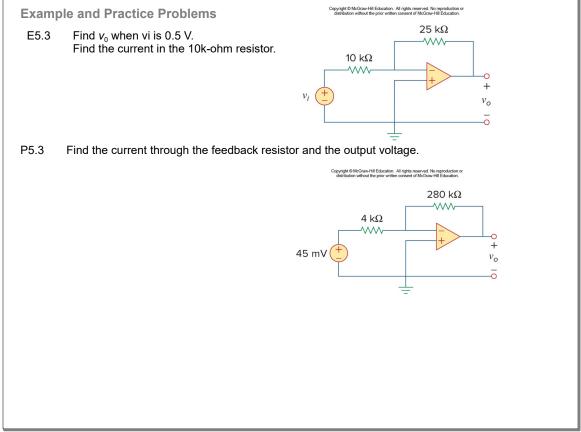


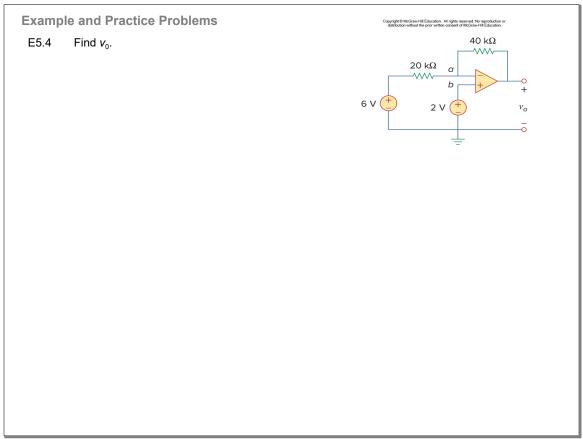






Ch 5, p5





Ch 4, p4

